# Application/Control No. O9/805,137 Examiner A. M. Thompson Applicant(s)/Patent Under Reexamination MOORE ET AL. Page 1 of 1

# Notice of References Cited

### **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-2003/0070013	04-2003	Hansson	710/59
	В	US-6,397,170	05-2002	Dean et al.	703/14
	C	US-			
	D	US-			
	Е	US-			
	F	US-			
	G	US-		·	
	Н	US-			
	1	US-	-		
	J	US-			
	К	US-			
	L	US-			
	М	US-			\`

## FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q					_
	R					
	S					
	Т					

### **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	C-L. Su et al., Low Power Architecture Design and Compilation Techniques for High-Performance Processors, Compcon Sprin '94, Digest of Papers, pages 489-498, February 1994.			
	V				
	w				
	x				

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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